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Bruce S. Itchkawitz, Ph.D.
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January 6, 2006

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: Title: **ARRANGEMENT OF INTEGRATED
CIRCUITS IN A MEMORY MODULE**
Letters Patent No. 6,930,900 B2
Issued: August 16, 2005
Our Reference: NETL.001DV4

I hereby certify that this correspondence and all
marked attachments are being deposited with the
United States Postal Service as first class mail in
an envelope addressed to: Commissioner for
Patents, P.O. Box 1450, Alexandria, VA 22313-
1450, on

1/6/06
(Date)

Bruce S. Itchkawitz, Reg. No. 47,677

Dear Sir:

Enclosed for filing is a Certificate of Correction in connection with the above-identified patent.

As the errors cited in the Certificate of Correction were incurred through the fault of the Patent Office, no fee is believed to be required. However, please charge our Deposit Account No. 11-1410 for any fees that may be incurred with this request.

Respectfully submitted,

Knobbe, Martens, Olson & Bear, LLP

Bruce S. Itchkawitz
Registration No. 47,677
Customer No. 20,995

**Certificate
JAN 13 2006
of Correction**

Enclosure

2268917
010606

JAN 13 2006

San Diego
619-235-8550

San Francisco
415-954-4114

Los Angeles
310-551-3450

Riverside
951-781-9231

San Luis Obispo
805-547-5580

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO. : 6,930,900 B2

DATED : August 16, 2005

INVENTORS : Jayesh R. Bhakta et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Col. 10, Line 34, Claim 13, after "portion," delete – o – and insert – of –.

MAILING ADDRESS OF SENDER:

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PATENT NO. 6,930,900 B2

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